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DB: US-PGPUB;USPAT;EPO;JPD;DERWENT;IBM_TO8

Default operator: **OR**

7. **Prüfung**

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	U	□	□	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C
1	□	□	□	US 6115283 A	20000905	36	Semiconductor device with programming capacitance element	365/149	365/200		Hidaka; Hideto	□	□
2	□	□	□	US 6438059 B2	20020820	16	Fuse programming circuit for programming fuses	365/225.7	267/E27.052; 377/526.1		Akita; Hironobu et al.	□	□
3	□	□	□	US 6163488 A	20001219	31	Semiconductor device with antifuse	365/200	365/225.7; 365/196		Tanizaki; Hiroaki et al.	□	□
4	□	□	□	US 6424684 B1	20020723	16	Redundancy antifuse bank for a memory device	365/225.7	365/185.09; 365/185.25		Seyyedy; Mirmajid	□	□

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